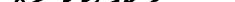


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				First Named Inventor	Yeo, et al.
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Sheet	1	of	1	Attorney Docket Number	TSM03-0826

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	
DF	4	TAVEL, B., et al., "Totally Silicided (CoSi ₂) Polysilicon: A Novel Approach To Very Low-Resistive Gate (~2Ω□) Without Metal CMP Nor Etching" IEDM (March 2001) pp. 825-828.	72
DF	5	MASZARA, W.P., et al., "Transistors With Dual Work Function Metal Gates By Single Full Silicidation (FUSI) Of Polysilicon," IEDM (February 2002) pp. 367-370.	

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